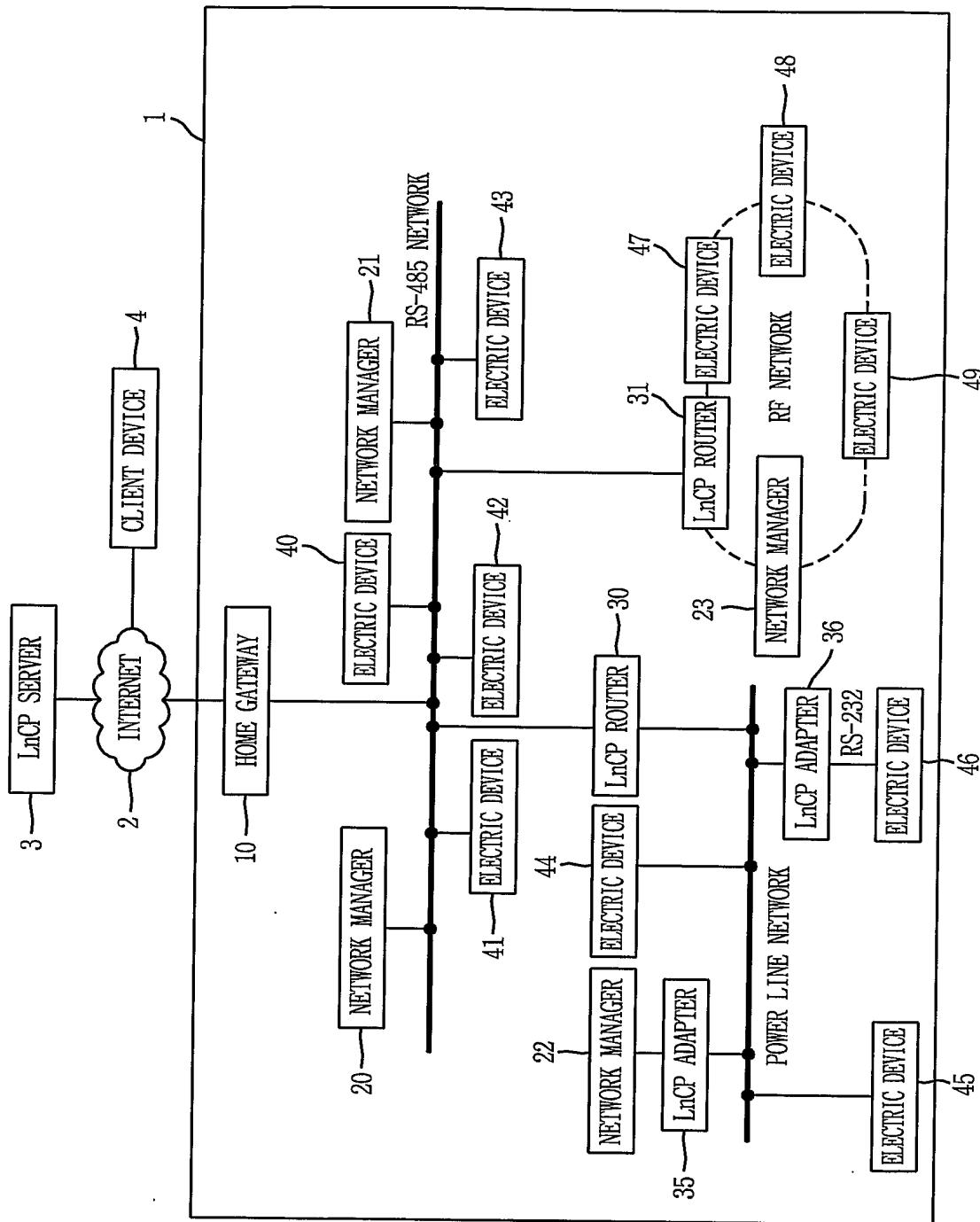


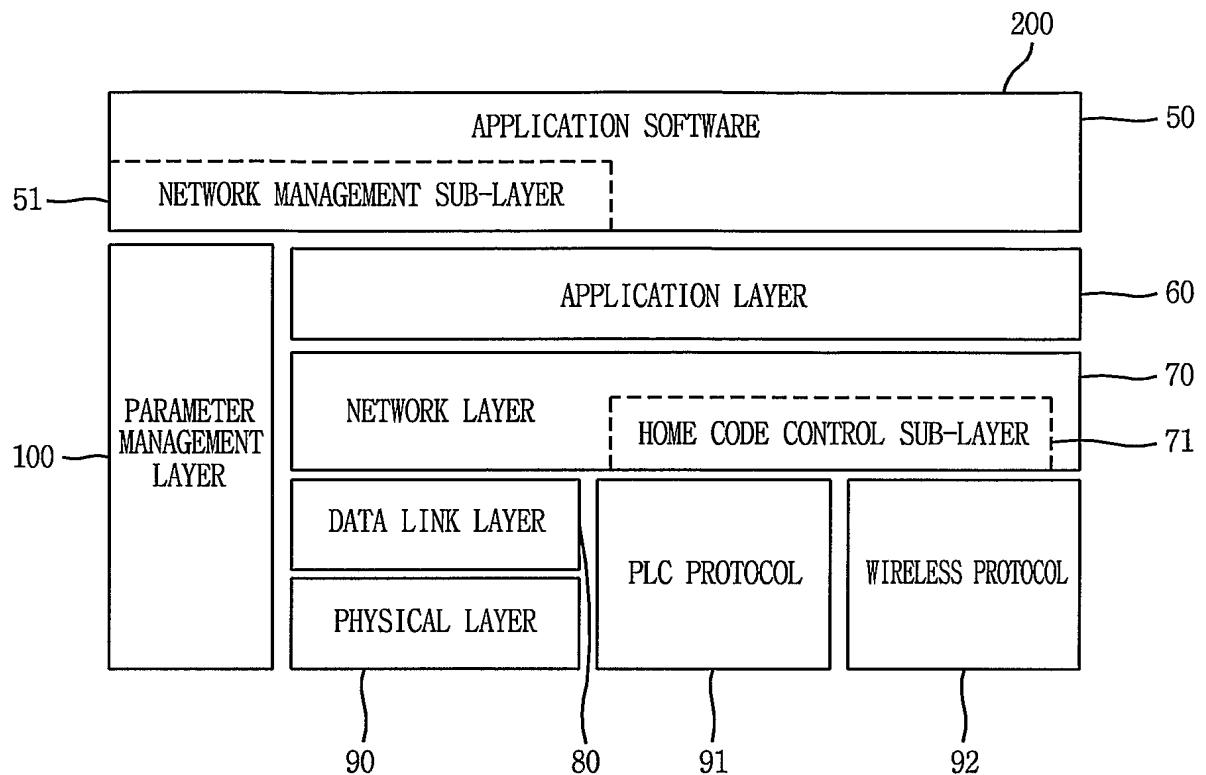
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FIG. 1



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FIG.2



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FIG.3

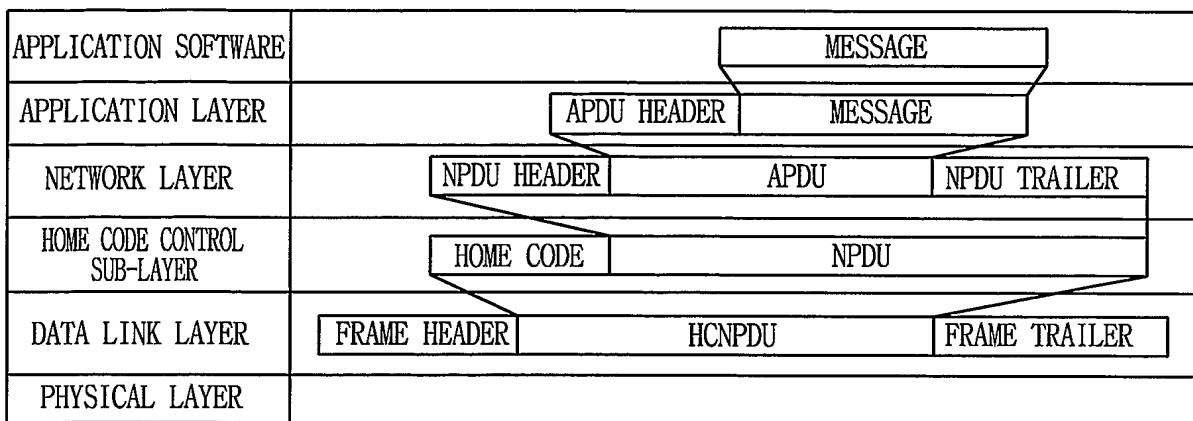
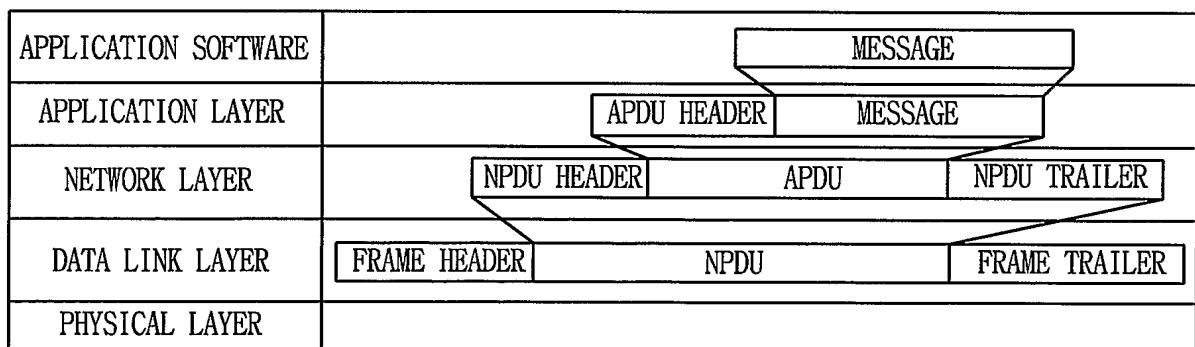


FIG.4



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FIG.5

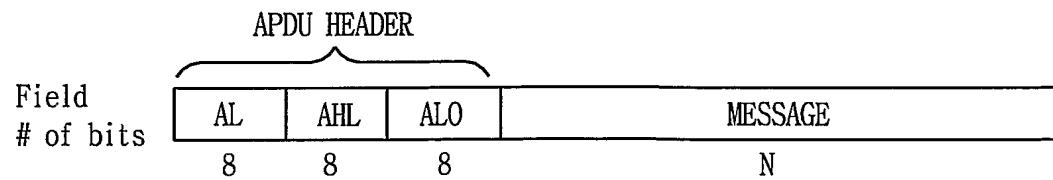


FIG.6

Field # of bits	SLP	DA	SA	PL	NLC	APDU	CRC	ELP
	8	16	16	8	24	N	16	8

FIG.7

Field # of bits	SP	NHL	PV	NPT	TC	PN
	3	5	8	4	2	2

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FIG.8



FIG.9

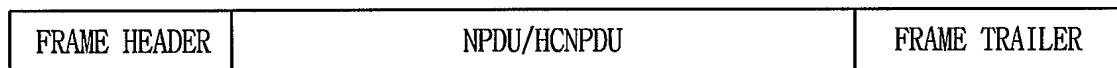
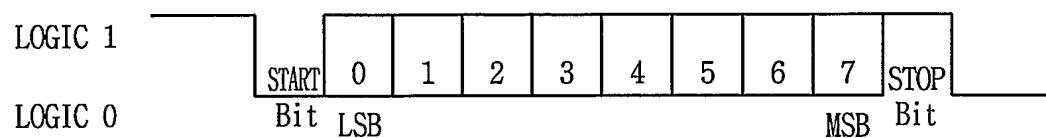


FIG.10



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FIG.11

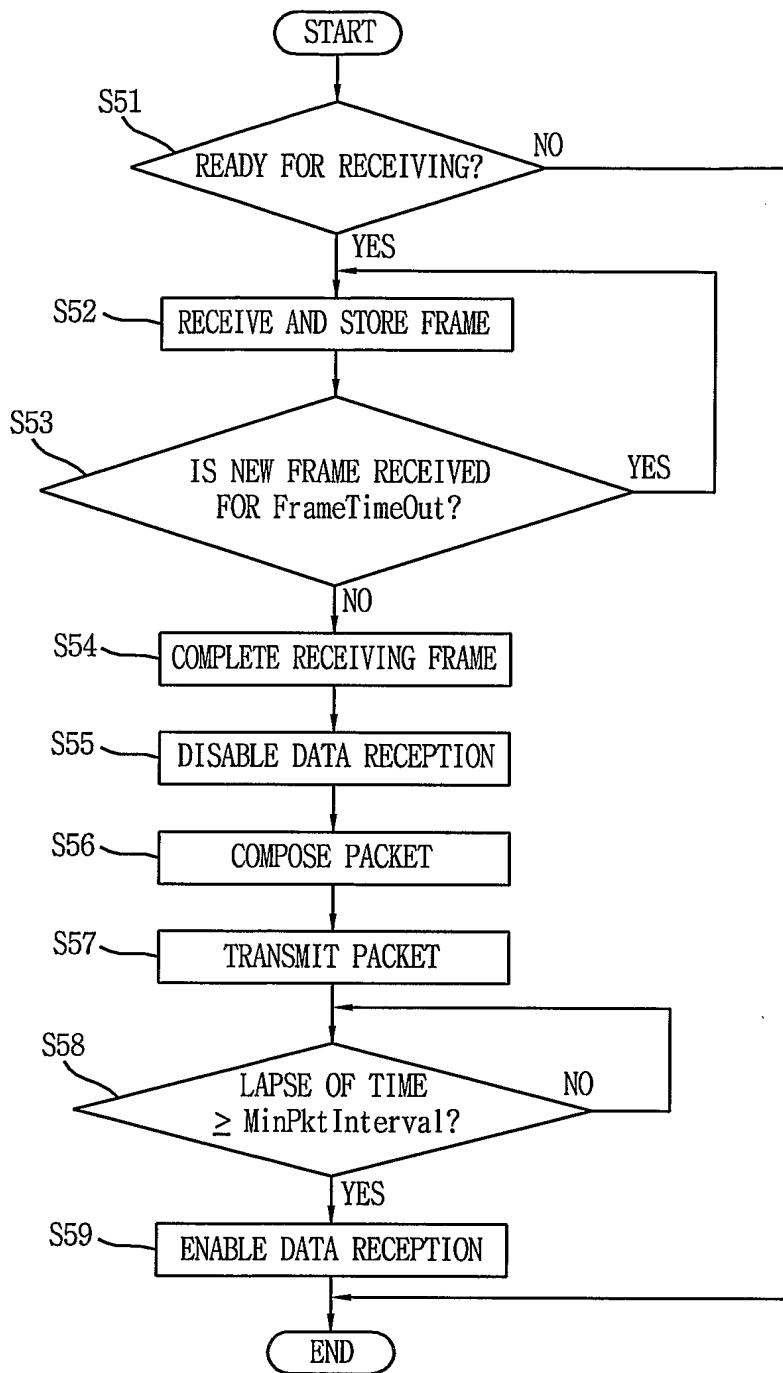
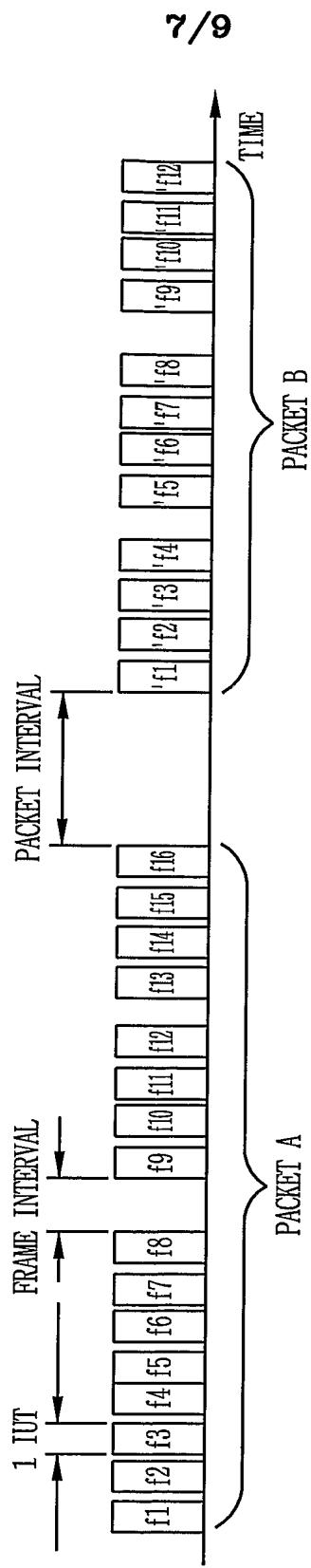
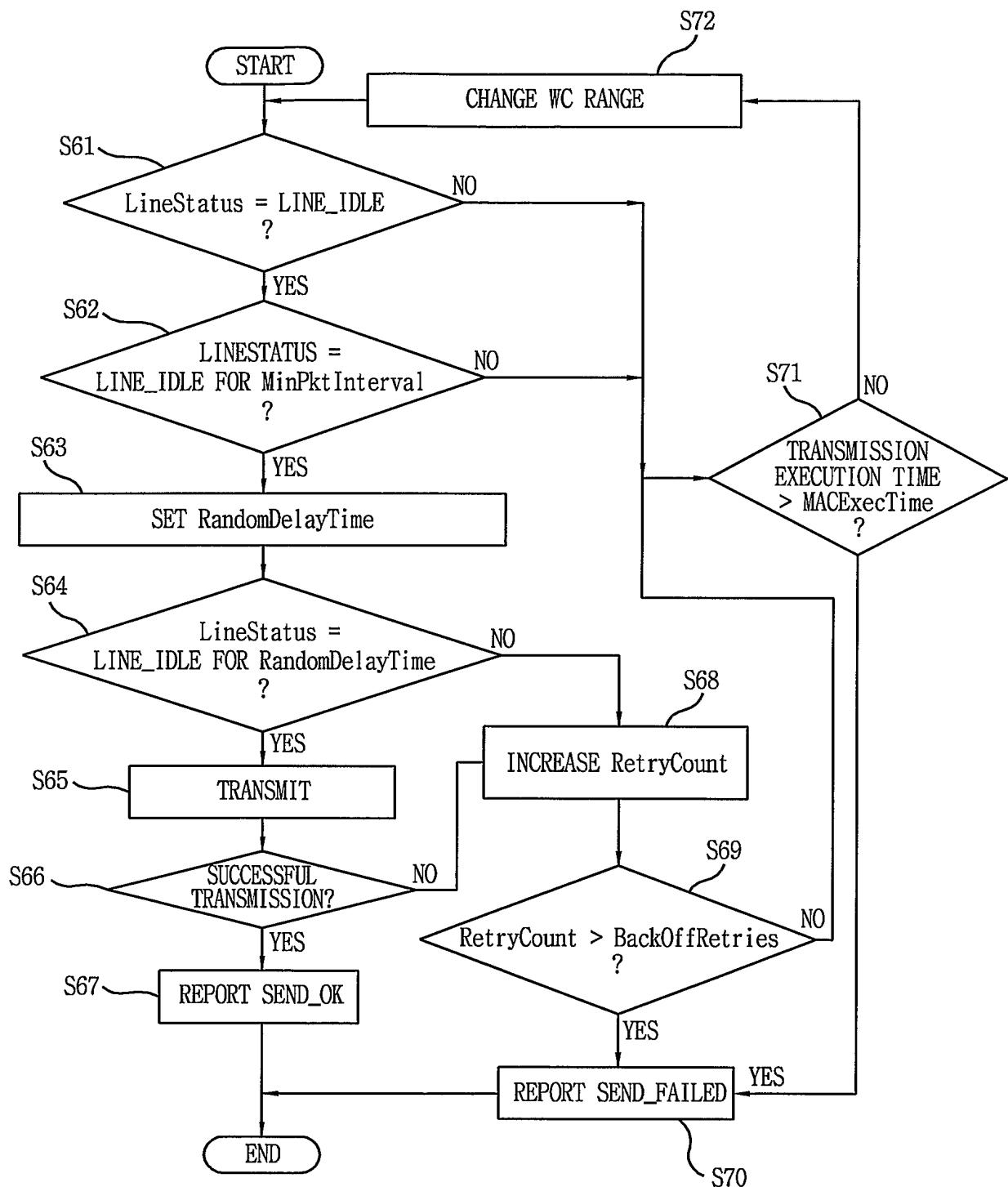


FIG. 12



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FIG. 13



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FIG. 14

